

| | L # | Hits | Search Text | DBs |
|----|-----|--------|--|--------------------|
| 1 | L1 | 5784 | front NEAR10 (host or server) | US-PGPUB; USPAT |
| 2 | L2 | 186541 | real NEAR5 time | US-PGPUB; USPAT |
| 3 | L3 | 12347 | dedicat\$5 NEAR5 processor\$4 | US-PGPUB; USPAT |
| 4 | L4 | 13445 | suspend\$4 NEAR10 (transfer\$4 or transmission\$4 or communicat\$4) | US-PGPUB; USPAT |
| 5 | L5 | 84 | 4 and 1 | US-PGPUB; USPAT |
| 6 | L6 | 1923 | 4 and 2 | US-PGPUB; USPAT |
| 7 | L7 | 291 | 4 and 3 | US-PGPUB; USPAT |
| 8 | L8 | 18 | 4 SAME 3 | US-PGPUB; USPAT |
| 9 | L9 | 137 | 4 SAME 2 | US-PGPUB; USPAT |
| 10 | L10 | 13 | 1 SAME 3 | US-PGPUB; USPAT |
| 11 | L11 | 89 | 1 SAME 2 | US-PGPUB; USPAT |
| 12 | L12 | 4468 | 2 and 3 | US-PGPUB; USPAT |

| | L # | Hits | Search Text | DBs |
|----|-----|-------|------------------------------------|--------------------|
| 13 | L13 | 540 | 2 SAME 3 | US-PGPUB; USPAT |
| 14 | L14 | 11 | 13 and 1 | US-PGPUB; USPAT |
| 15 | L15 | 13 | 13 and 4 | US-PGPUB; USPAT |
| 16 | L16 | 95100 | game\$4 or gaming | US-PGPUB; USPAT |
| 17 | L17 | 122 | 16 SAME 1 | US-PGPUB; USPAT |
| 18 | L18 | 3918 | 16 SAME 2 | US-PGPUB; USPAT |
| 19 | L19 | 157 | 16 SAME 3 | US-PGPUB; USPAT |
| 20 | L20 | 46 | 16 SAME 4 | US-PGPUB; USPAT |
| 21 | L21 | 79 | 18 and 1 | US-PGPUB; USPAT |
| 22 | L22 | 214 | 18 and 3 | US-PGPUB; USPAT |
| 23 | L23 | 62 | 18 and 4 | US-PGPUB; USPAT |
| 24 | L24 | 8270 | 16 SAME (cellul\$6 or telephon\$4) | US-PGPUB; USPAT |

| | L # | Hits | Search Text | DBs |
|----|-----|-------|--|---------------------------|
| 25 | L25 | 143 | 24 and 1 | US-PGPUB; USPAT |
| 26 | L26 | 2200 | 24 and 2 | US-PGPUB; USPAT |
| 27 | L27 | 239 | 24 and 3 | US-PGPUB; USPAT |
| 28 | L28 | 109 | 24 and 4 | US-PGPUB; USPAT |
| 29 | L29 | 13110 | 2 SAME (cellul\$6 or telephon\$4) | US-PGPUB; USPAT |
| 30 | L30 | 11 | 29 SAME 1 | US-PGPUB; USPAT |
| 31 | L31 | 0 | bretcher-j\$40.inv. | US-PGPUB; USPAT |
| 32 | L32 | 8 | bretscher-j\$40.inv. | US-PGPUB; USPAT |
| 33 | L33 | 14 | ("4935870" "4949248" "5163131" "5392400" "5583993" "5630757" "5671377" "5778368" "6023685" "6029175" "6044205" "6075863" "6182109" "6192408").PN. | US-PGPUB; USPAT; USOCR |
| 34 | L34 | 2 | ("6370564").URPN. | USPAT |

| | L # | Hits | Search Text | DBs |
|----|-----|--------|---|---------------------------|
| 35 | L35 | 38 | ("4223380" "4372558" "4437168" "4570930" "4592546" "4769771" "4935870" "4949248" "4958835" "4998199" "5021949" "5051822" "5072381" "5163131" "5179517" "5218631" "5251909" "5263723" "5273288" "5292125" "5324035" "5340119" "5347477" "5349633" "5351970" "5355453" "5379296" "5397133" "5405151" "5423002" "5428528" "5428608" "5428782" "5442771" "5452468" "5475819" "5577210" "5588146").PN. | US-PGPUB; USPAT; USOCR |
| 36 | L36 | 6 | ("6175854").URPN. | USPAT |
| 37 | L37 | 917 | PKT | US-PGPUB; USPAT |
| 38 | L38 | 26 | PKT NEAR10 tech\$10 | US-PGPUB; USPAT |
| 39 | L39 | 709 | front NEAR10 (host or server) | EPO; JPO; DERWENT |
| 40 | L40 | 75610 | real NEAR5 time | EPO; JPO; DERWENT |
| 41 | L41 | 837 | dedicat\$5 NEAR5 processor\$4 | EPO; JPO; DERWENT |
| 42 | L42 | 4804 | suspend\$4 NEAR10 (transfer\$4 or transmission\$4 or communicat\$4) | EPO; JPO; DERWENT |
| 43 | L43 | 154718 | game\$4 or gaming | EPO; JPO; DERWENT |

| | L # | Hits | Search Text | DBs |
|----|-----|--------|--------------------------|----------------------|
| 44 | L44 | 585456 | cellul\$6 or telephon\$4 | EPO; JPO; DERWENT |
| 45 | L45 | 17 | 39 and 40 | EPO; JPO; DERWENT |
| 46 | L46 | 1 | 39 and 41 | EPO; JPO; DERWENT |
| 47 | L47 | 0 | 39 and 42 | EPO; JPO; DERWENT |
| 48 | L48 | 15 | 39 and 43 | EPO; JPO; DERWENT |
| 49 | L49 | 39 | 39 and 44 | EPO; JPO; DERWENT |
| 50 | L50 | 232 | 42 and 44 | EPO; JPO; DERWENT |
| 51 | L51 | 30 | 42 and 43 | EPO; JPO; DERWENT |
| 52 | L52 | 0 | 42 and 41 | EPO; JPO; DERWENT |
| 53 | L53 | 30 | 42 and 40 | EPO; JPO; DERWENT |
| 54 | L54 | 14 | 41 and 43 | EPO; JPO; DERWENT |
| 55 | L55 | 53 | 41 and 44 | EPO; JPO; DERWENT |

| | L # | Hits | Search Text | DBs |
|----|-----|------|---|----------------------------------|
| 56 | L56 | 56 | 41 and 40 | EPO; JPO; DERWENT |
| 57 | L57 | 5509 | 43 and 44 | EPO; JPO; DERWENT |
| 58 | L58 | 3 | 57 and 41 | EPO; JPO; DERWENT |
| 59 | L59 | 6 | 57 and 42 | EPO; JPO; DERWENT |
| 60 | L60 | 3 | ibretscher-j\$40.inv. | EPO; JPO; DERWENT; IBM_TDB |
| 61 | L61 | 0 | PKT NEAR10 technolog\$10 | EPO; JPO; DERWENT; IBM_TDB |
| 62 | L62 | 20 | front NEAR10 (host or server) | IBM_TDB |
| 63 | L63 | 1184 | real NEAR5 time | IBM_TDB |
| 64 | L64 | 48 | dedicat\$5 NEAR5 processor\$4 | IBM_TDB |
| 65 | L65 | 35 | suspend\$4 NEAR10 (transfer\$4 or transmission\$4 or communicat\$4) | IBM_TDB |
| 66 | L66 | 105 | game\$4 or gaming | IBM_TDB |
| 67 | L67 | 1234 | cellul\$6 or telephon\$4 | IBM_TDB |
| 68 | L68 | 13 | 67 and 66 | IBM_TDB |
| 69 | L69 | 57 | 67 and 63 | IBM_TDB |
| 70 | L70 | 11 | 66 and 63 | IBM_TDB |

Access DB#

149055

SEARCH REQUEST FORM

Scientific and Technical Information Center

Requester's Full Name: Pinchus Laufer Examiner #: 73139 Date: 3/24/05
 Art Unit: 2100 Phone Number 272-3599 Serial Number: 10/821,833
 Mail Box Location: 1C81 Results Format Preferred (circle): PAPER DISK E-MAIL

If more than one search is submitted, please prioritize searches in order of need.

 Please provide a detailed statement of the search topic, and describe as specifically as possible the subject matter to be searched. Include the elected species or structures, keywords, synonyms, acronyms, and registry numbers, and combine with the concept or utility of the invention. Define any terms that may have a special meaning. Give examples or relevant citations, authors, etc, if known. Please attach a copy of the cover sheet, pertinent claims, and abstract.

Title of Invention: _____

Inventors (please provide full names): _____

Earliest Priority Filing Date: _____

**For Sequence Searches Only* Please include all pertinent information (parent, child, divisional, or issued patent numbers) along with the appropriate serial number.*

Litigation
 6,370,564

Inventor: John Bretscher

O.G. Date August 3, 2004

STAFF USE ONLY

Searcher: Shirelle Green

Searcher Phone #: 306-4767

Searcher Location: 2100

Date Searched: MAR 28 2005

Date Completed: _____

Searcher Prep & Review Time: _____

Clerical Prep Time: _____

Online Time: 10

Type of Search

Sequence (#) _____

AA Sequence (#) _____

Structure (#) _____

Bibliographic _____

Litigation ☒

Fulltext _____

Patent Family _____

Other _____

Vendors and cost where applicable

STN

Dialog _____

Questel/Orbit 11.58

Dr.Link _____

Lexis/Nexis

Sequence Systems _____

WWW/Internet

Other (specify) _____

1 of 1 DOCUMENT

UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT

6370564

[Link to Claims Section](#)

April 9, 2002

Computer system architecture and method for multi-user, real-time applications

REISSUE: April 9, 2004 - Reissue Application filed Ex. Gp.: 2152; Re. S.N. 10/821,833 (O.G. August 3, 2004)

INVENTOR: Bretscher, John - Elgin, Illinois

APPL-NO: 055523 (09)

FILED-DATE: February 12, 2001

GRANTED-DATE: April 9, 2002

ASSIGNEE-AT-ISSUE: Ameritech, Services, Inc., Hoffman Estates, Illinois, 02

ASSIGNEE-AFTER-ISSUE: April 9, 2004 - ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS), PKT TECHNOLOGIES, LLC 195 N. HARBOR DRIVE SUITE 5403 CHICAGO ILLINOIS 60601, Reel and Frame Number: 15215/0014

CORE TERMS: user, server, dedicated, architecture, processor, front-end, modem, station, processing, network ...

ENGLISH-ABST:

An architecture is disclosed for a computer system that runs applications serving multiple users. The computer system includes multiple processors, some of which run quick applications, i.e., requiring real time response, while others run applications with less stringent requirements. Each real time processor can be dedicated to running just one instance of an application. The processors can be of disparate types running disparate operating systems and optimized for disparate applications. The system is centrally controlled with the processors communicating among themselves over a shared LAN or via a communications switch. The system may also facilitate simultaneous voice and data communications among users. Users communicate with the system using any of a number of standard techniques: including dial-up telephone lines, ISDN, packet access services, ADSL, cable TV and the like.

PARENT-PAT-INFO:

This application is a division of application Ser. No. 08/661,428, filed Jun. 11, 1996, now U.S. Pat. No. 6,175,854, filed Jan. 16, 2001.

LEXIS-NEXIS
Library: PATENTS
File: ALL

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File: CASES

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LEXIS-NEXIS
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LEXIS-NEXIS
Library: NEWS
File: CURNWS

us6370564/pn

** SS 2: Results 1

Search statement 3

?prt full nonstop legalall

1/1 PLUSPAT - (C) QUESTEL-ORBIT- image
PN - US2001039569 A1 20011108 [US20010039569]
PN2 - US6370564 B2 20020409 [US6370564]
TI - (A1) COMPUTER SYSTEM ARCHITECTURE AND METHOD FOR MULTI-USER REAL-TIME APPLICATION
PA - (B2) AMERITECH SERVICES INC (US)
PA0 - Ameritech, Services, Inc., Hoffman Estates IL [US]
PA2 - (B2) AMERITECH SERVICES INC (US)
IN - (A1) BRETSCHER JOHN (US)
AP - US5552398 19980406 [1998US-0055523]
FD - Division of: US6175854 - 20010116
PR - US5552398 19980406 [1998US-0055523]
- US66142896 19960611 [1996US-0661428]
IC - (A1) G06F-015/16
EC - H04L-029/06
- H04L-029/06C2
ICO - T04L-029/06C8
PCL - ORIGINAL (O) : 709205000; CROSS-REFERENCE (X) : 709203000 709219000
DT - Corresponding document
CT - US4935870; US4949248; US5163131; US5392400; US5583993; US5630757;
US5671377; US5778368; US6023685; US6029175; US6044205; US6075863;
US6182109; US6192408
STG - (A1) Utility Patent Application published on or after January 2, 2001
STG2- (B2) U.S. Patent (with pre-grant pub.) after Jan. 2, 2001
AB - An architecture is disclosed for a computer system that runs applications serving multiple users. The computer system includes multiple processors, some of which run quick applications, i.e., requiring real time response, while others run applications with less stringent requirements. Each real time processor can be dedicated to running just one instance of an application. The processors can be of disparate types running disparate operating systems and optimized for disparate applications. The system is centrally controlled with the processors communicating among themselves over a shared LAN or via a communications switch. The system may also facilitate simultaneous voice and data communications among users. Users communicate with the system using any of a number of standard techniques: including dial-up telephone lines, ISDN, packet access services, ADSL, cable TV and the like.
UP - 2001-46

1/2 LGST - (C) EPO
PN - US2001039569 A1 20011108 [US20010039569]
- US6370564 B2 20020409 [US6370564]
AP - US5552398 19980406 [1998US-0055523]
ACT - 20030923 US/CC-A
CERTIFICATE OF CORRECTION
- 20040803 US/RF-A
REISSUE APPLICATION FILED
EFFECTIVE DATE: 20040409
UP - 2004-34

2/2 LGST - (C) EPO
PN - US2001039569 A1 20011108 [US20010039569]

- US6370564 B2 20020409 [US6370564]
AP - US5552398 19980406 [1998US-0055523]
ACT - 20021022 US/CC-A
CERTIFICATE OF CORRECTION
UP - 2003-22

1/1 CRXX - (C) CLAIMS/RRX
PN - 6,370,564 A 20020409 [US6370564]
PA - Ameritech Services Inc
ACT - 20040409 REASSIGNED
ASSIGNMENT OF ASSIGNORS INTEREST

Assignor: AMERITECH SERVICES, INC., DATE SIGNED: 03/29/2004

Assignee: PKT TECHNOLOGIES, LLC, 195 N. HARBOR DRIVE, SUITE 5403,
CHICAGO, ILLINOIS, 60601

Reel 015215/Frame 0014

Contact: PETER K. TRZYNA, P.O. BOX 7131, CHICAGO, IL 60680

- 20040409 REISSUE REQUESTED
ISSUE DATE OF O.G.: 20040803
REISSUE REQUEST NUMBER: 10/821833
EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2152

Reissue Patent Number:

- 20040511 REASSIGNED
ASSIGNMENT OF ASSIGNORS INTEREST

Assignor: AMERITECH SERVICES, INC., DATE SIGNED: 03/29/2004

Assignee: PKT TECHNOLOGIES, LLC, 195 N. HARBOR DRIVE, SUITE 5403,
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Reel 015642/Frame 0087

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